

**Self Financed
Short Term Training Program (STTP) on
“Workshop on VLSID-SoC & Micro-nano
Technologies”**

Special Emphasis on

Communication Hardware Architecture and
Applications

18th to 22nd December, 2017

REGISTRATION FORM

(Please use separately attached Registration form for
sending the application)

Name:

Designation:.....

Organization:.....

Qualification:

Correspondence Address:

.....

Tel(M)

Type of participation:.....

E-Mail:

Registration Fee is paid in the form of

DD/Online Transfer to Institute's Account.

Amount of Rs..... DD No.....

Date..... Name of the Bank

Accommodation Required: Yes / No

Date: Place:

Signature

Note:

- Demand draft should be made in favour of
Director, NIT Raipur, payable at Raipur.
- The detail for online bank-transfer is
attached with the registration form.

CHIEF-PATRON

Dr A M Rawani

Director, NIT Raipur

PATRON

Prof S Sanyal

Dean (R&C), NIT Raipur

Convener

Prof S Verma

Professor

Dr. A. S. Raghuvanshi

HoD-ETC

COORDINATORS

Dr B Acharya

Assistant Professor

Dr S Majumder

Assistant Professor

Dr A Naugarhiya

Assistant Professor

ORGANIZING COMMITTEE

Prof S Verma

Dr A S Raghuvanshi

Dr B Acharya

Dr S Majumder

Mr S Chakroborty

Mr A Gupta

Dr S K Saha

Dr T Meenpal

Dr R K Chaurasiya

Dr A Naugarhiya

INVITED SPEAKERS (Tentative)

Prof R M Patrikar, VNIT Nagpur

Dr Vinay Kumar, VNIT Nagpur

ADDRESS FOR CORRESPONDENCE

Dr A Naugarhiya

(8989828339)

anaugarhiya.etc@nitrr.ac.in

**Department of Electronics and
Telecommunication**

National Institute of Technology, Raipur

G.E. Road, Raipur, India-492 010

Website: www.nitrr.ac.in

**Self Financed One Week
Short Term Training Program
on**

**VLSID-SoC & Micro-nano
Technologies**

Special Emphasis on

Communication Hardware Architecture,
Applications and IoT

18th to 22nd December, 2017



Organized by

**Department of Electronics and
Telecommunication Engineering**



**National Institute of Technology
Raipur, India-492 010**

ABOUT THE INSTITUTE

National Institute of Technology Raipur situated in the capital of Chhattisgarh, has proven to be "avant-grade" in the field of science and technology over past few decades in this region. With sweet memory of foundation ceremony by our president Hon'ble Dr. Rajendra Prasad on 14th September 1956, the institute started with two departments namely Metallurgical and Mining Engineering. Later the inauguration of the Institute building was done by our Prime Minister Hon'ble Pt. Jawahar Lal Nehru on 14th March 1963. From 1st December 2005, the institute has become the National Institute of Technology. It is well connected with Mumbai, Delhi and all metro cities by regular flights and is on the main Howrah-Mumbai railway route. The institute is 5 km from the Raipur railway station and 18 km from airport on NH-6, the Great Eastern Road.

ABOUT THE DEPARTMENT

The **Department of Electronics and Telecommunication** came into existence in 1985. The department aims to be a national leader in imparting quality education, carrying out research and technology development in the field of Electronics and Telecommunication Engineering. The department provides an outstanding research environment and offers academic program leading to the award of B.Tech, M.Tech (VLSI and Embedded System) and PhD degree.

VENUE

Department of Electronics and
Telecommunication Engineering
NIT Raipur (CG) – 492 010, INDIA

OBJECTIVES

VLSI-SoC & Micro-nano Technologies which include Microelectronics & VLSI Design, System on Chip design & development and nano electronics have a significant role in engineering and manufacturing field across all engineering disciplines.

As such, it has wide applications in various program recently launched by Govt. of India, for example; Make in India, Smart Cities, Digital India etc.

The aim of this training programme is to provide an exposure to both basics and recent advances in VLSI and Micro-nano Technologies to the teaching and research community associated with the Departments of Electronics, Electrical and Computer Science etc. In addition, course will emphasize digital communication and its VLSI implementation.

Expert lectures will be delivered by faculty and scientists from various academic institutes, including IISc, IITs, IIITs, NITs and the host institute (NIT Raipur). Hands-On Sessions will be suitably designed to supplement classroom discussions using EDA tools.

ELIGIBILITY

Participation in this short term training programme (STTP) is open to the undergraduate students, post-graduate students, faculty members, engineers, researchers, and other executives working on diverse fields in Electronics Engineering, Computer Engineering, Electrical Engineering, Instrumentation Engineering, Biomedical Engineering, Biotechnology, etc.

SELECTION

The course coordinators reserve the right of selection of participants. The preference will be given to the participants from outside the institute (NIT Raipur). Intimation regarding selection will be sent to the candidates via email as per the schedule. Certificates will be issued to the participants only after attending the complete course. No TA/DA will be paid to the participants. However, paid accommodation may be provided to outside candidates at the institute hostels, provided request for the same is made in the registration form, well in advance.

TOPICS TO BE COVERED

- Introduction of IoT and Application in Smart city.
- Recent Research Trends in Field of VLSI & Micro-nano Technologies
- ASIC Design Flow.
- Introduction of RF and MEMS.
- Introduction of industry standard EDA tools.
- Interface circuits for On Chip MEMS Based Sensor and Actuators.
- Codes and Hardware Architecture.
- Software defined Radio Architecture and Systems.
- Magnetic Induction Communication for non-conventional media application.

IMPORTANT DATES

Registration

On or before **30.11.2017, 10.12.2017**

Intimation of acceptance

On or before **10.12.2017, 11.12.2017**

REGISTRATION FEE DETAILS

Participants	Amount (in Rs)
UG Students	1000/-
PG/PhD Students	2000/-
Faculty	4000/-
NIT Student	1000/-

Application in the prescribed format with registration fee in the form of DD (drawn in favor of "Director, NIT Raipur") or proof of online transfer to the institute account must reach the coordinator (Dr A. Naugarhiya) on or before 10th December, 2017. Registration fee includes registration kit and tea during the program. Fee is non-refundable for selected candidates.

Self-Sponsored-One Week
Short Term Training Program (STTP) on

VLSID-SoC & Micro-nano Technologies
Special Emphasis on
Communication Hardware Architecture and Applications

18th to 22nd December, 2017

REGISTRATION FORM

Name:

.....(M/F).....

Designation:

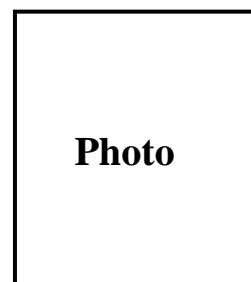
Organization:

Qualification:

Correspondence Address:

.....

Tel. (M) E-Mail:



Fee Type: (Please tick on appropriate-box)

UG Student	<input type="checkbox"/>	PG/PhD Student	<input type="checkbox"/>	Faculty /Industry Delegates	<input type="checkbox"/>	NIT Student	<input type="checkbox"/>
------------	--------------------------	-------------------	--------------------------	--------------------------------	--------------------------	-------------	--------------------------

DD-Details:

(If fee is paid by **DD** in favor of **Director, NIT Raipur** payable at **Raipur**)

Amount:.....DD Number.....:.....Date:.....

Name of the Bank.....

Online Transfer Detail:

Amount:.....Transaction/Ref. Number:.....Date:.....

Name of the Bank.....

(Please attach a proof of amount transfer with this form)

Accommodation required (in institute hostel, subject to availability): Yes / No

(Participant will have to pay an additional amount of **Rs. 200/-** per day at institute hostel towards his boarding and food)

Date:

Place:

Signature

Note: DD/Online Payment proof along with the registration-form must reach the coordinator (**Dr A. Naugarhiya**) on or before **10th Dec, 2017**.

Account Details for Online payment



राष्ट्रीय प्रौद्योगिकी संस्थान रायपुर
NATIONAL INSTITUTE OF TECHNOLOGY RAIPUR
(Institute of National Importance)
G.E. Road, Raipur - 492010 (C.G.)

Phone: (0771) 22 54 200
Fax: (0771) 22 54 600
Email: director.nitr@rediffmail.com
Website: www.nitr.ac.in

Bank Details of NIT Raipur is as under:

Bank Name:	State Bank of India
Bank Branch:	G.C.E.T. Branch
Bank Address:	G.E. Road, Raipur. (C.G.) Pin - 492010.
City:	Raipur (C.G.)
Country:	India
Account No:	30054181310
Account Name:	Director NIT Raipur
Bank Branch Code:	02852
IFSC Code:	SBIN0002852
Swift Code:	SBININBB646
Phone No. of Branch:	0771- 4040791/795

Dy. Registrar (F&A)
NIT Raipur (C.G.)

23/03/15